

1/29/80
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group 222
Examiner R. A. Farley

Amendment
L. Washington
1-29

Arent H. Kits Van Heyningen

Dual Channel Delay Sensing Circuit (U)

Serial No. 944,530 ✓

Filed September 11, 1978

RAYTHEON COMPANY
EXECUTIVE OFFICES
141 SPRING STREET
LEXINGTON, MA 02173
DATE: 28 Dec 1979

JAN 20 1980

GROUP 220

RECEIVED

Hon. Commissioner of Patents and Trademarks

Washington, D.C. 20231

Sir:

DEC 31 1979
GROUP 220
LICENSING & REVIEW

AMENDMENT

(U) In response to the Office Action dated October 30, 1979,
please amend herein as follows:

In the Claims

- 1 (C) 1. (Amended) A delay measurement circuit comprising:
2 first and second channels for receiving first and second
3 signals, each of said signals having a leading edge and a
4 trailing edge;
5 a leading edge circuit coupled to said first and said
6 second channels for signaling the temporal relationship of
7 the leading edges of said first and said second signals;
8 a trailing edge circuit coupled to said first and said
9 second channels for signaling the temporal relationship of
10 the trailing edges of said first and said second signals;
11 a memory coupled to said leading edge circuit for storing
12 data of said leading edges;
13 timing means activated by said leading edge circuit for
14 measuring a delay between leading edges of said first and said
15 second signals; [and]

a'
CLASSIFIED BY NAV SEA INST. C58 11.5 of Oct. 1974
DOWNGRADE TO N.A. ON
DECLASSIFY ON Dec 31, 2008
REVIEW ON N.A.

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